

A DIGITAL ELECTRONIC CIRCUIT FOR TRANSLATING HIGH VOLTAGE LEVELS TO LOW VOLTAGE LEVELS

Abstract of the Disclosure

A voltage level translator for digital logic circuits provides high level to low level voltage translation with equal rise and fall delays. The
5 voltage level translator may include an input high voltage logic inverter (operating at the high voltage level) and connected to an output low voltage logic inverter operating at the low voltage level via a voltage reduction circuit. A related method for
10 providing high level to low voltage translation may include providing an input inverter operating at the high voltage level and an output inverter operating at the low voltage level. Furthermore, the output of the high voltage inverter may be coupled to the input of
15 the low voltage inverter after reducing the output voltage of the high voltage inverter to the required level.